

Title: STRAINED SEMICONDUCTOR SUBSTRATE AND PROCESSES THEREFOR

Inventor(s): Pelella et al.

Appl. No.: Unknown

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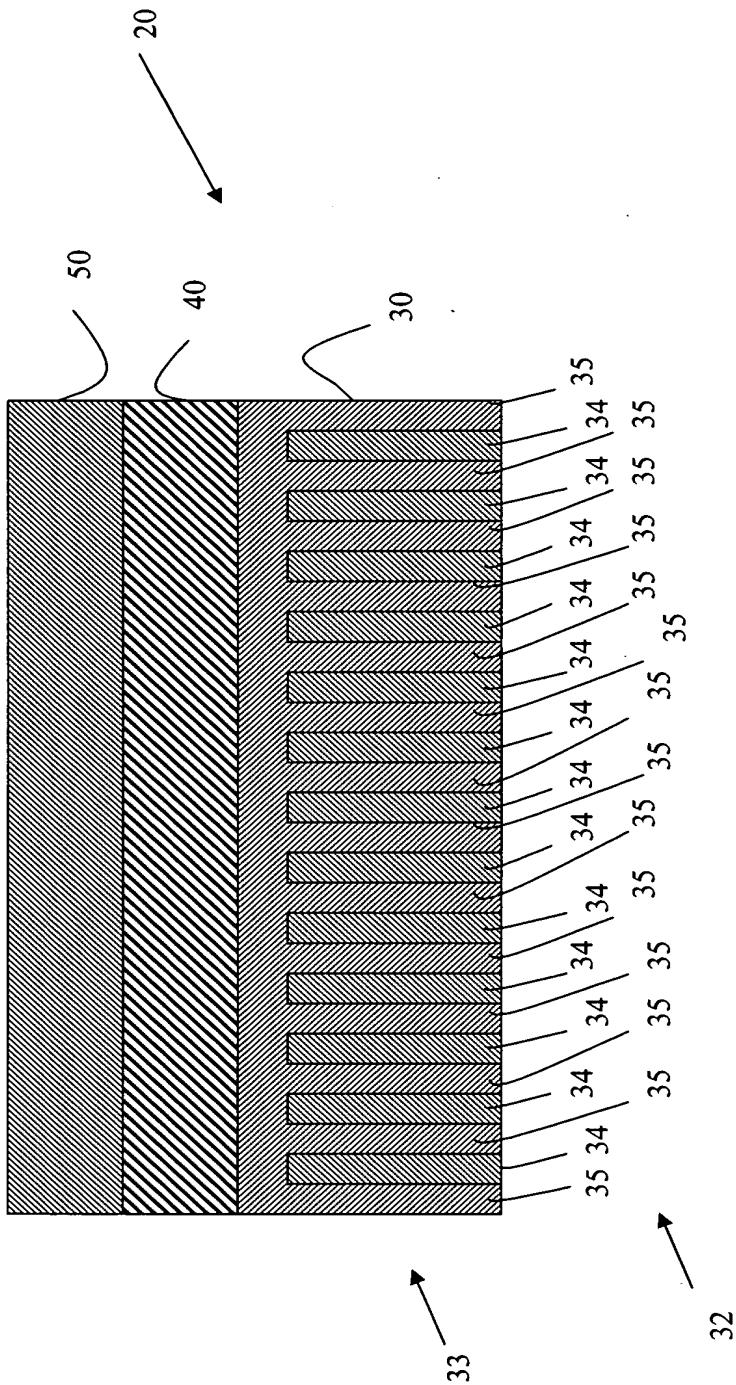


FIGURE 1

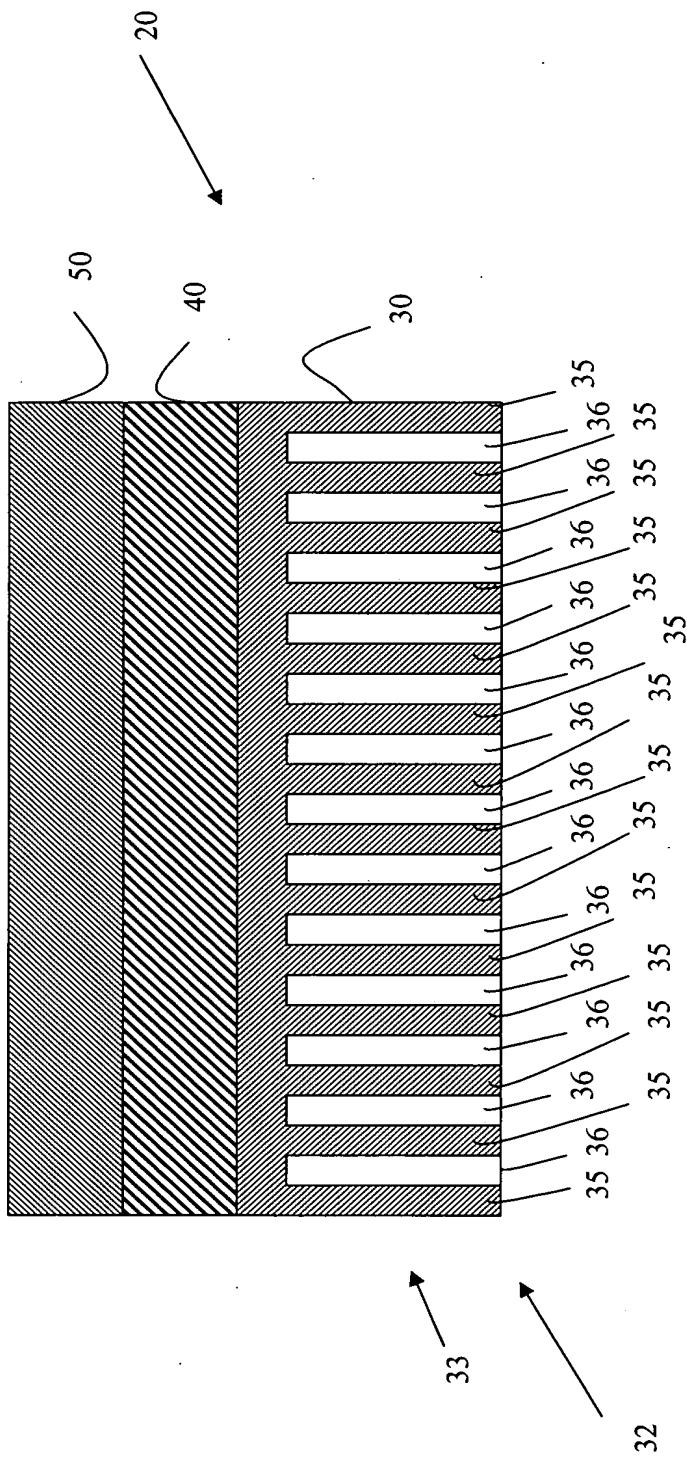


FIGURE 2

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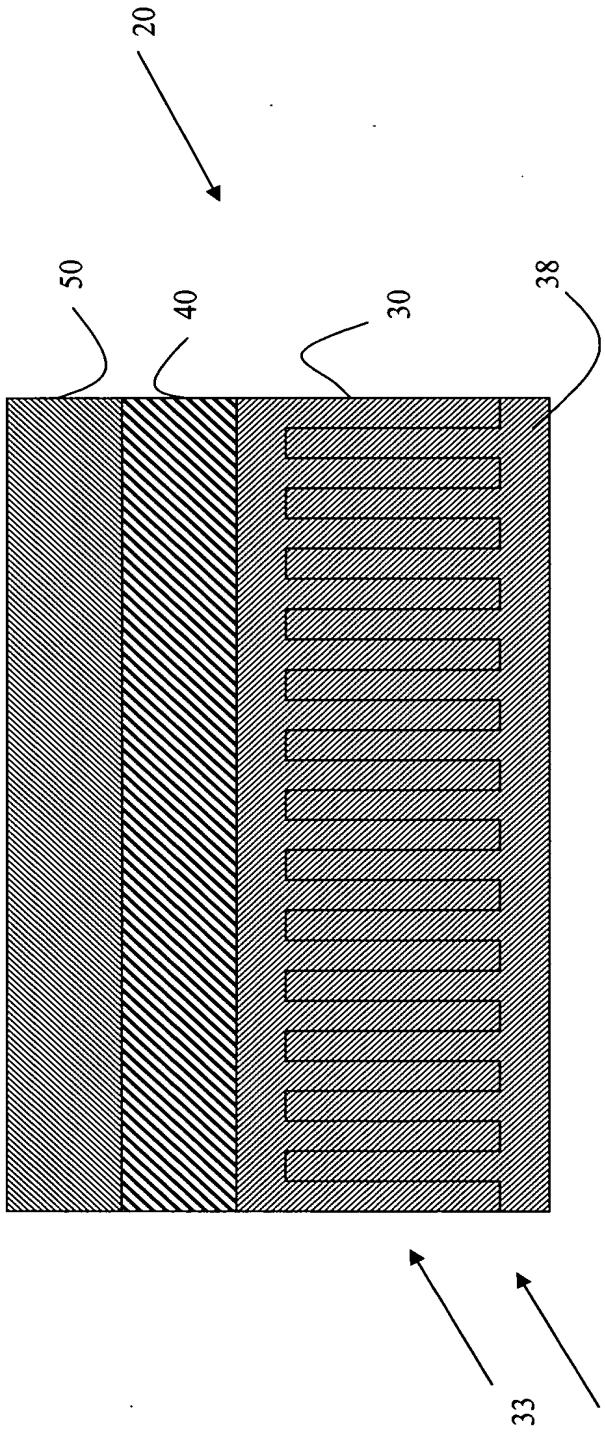


FIGURE 3

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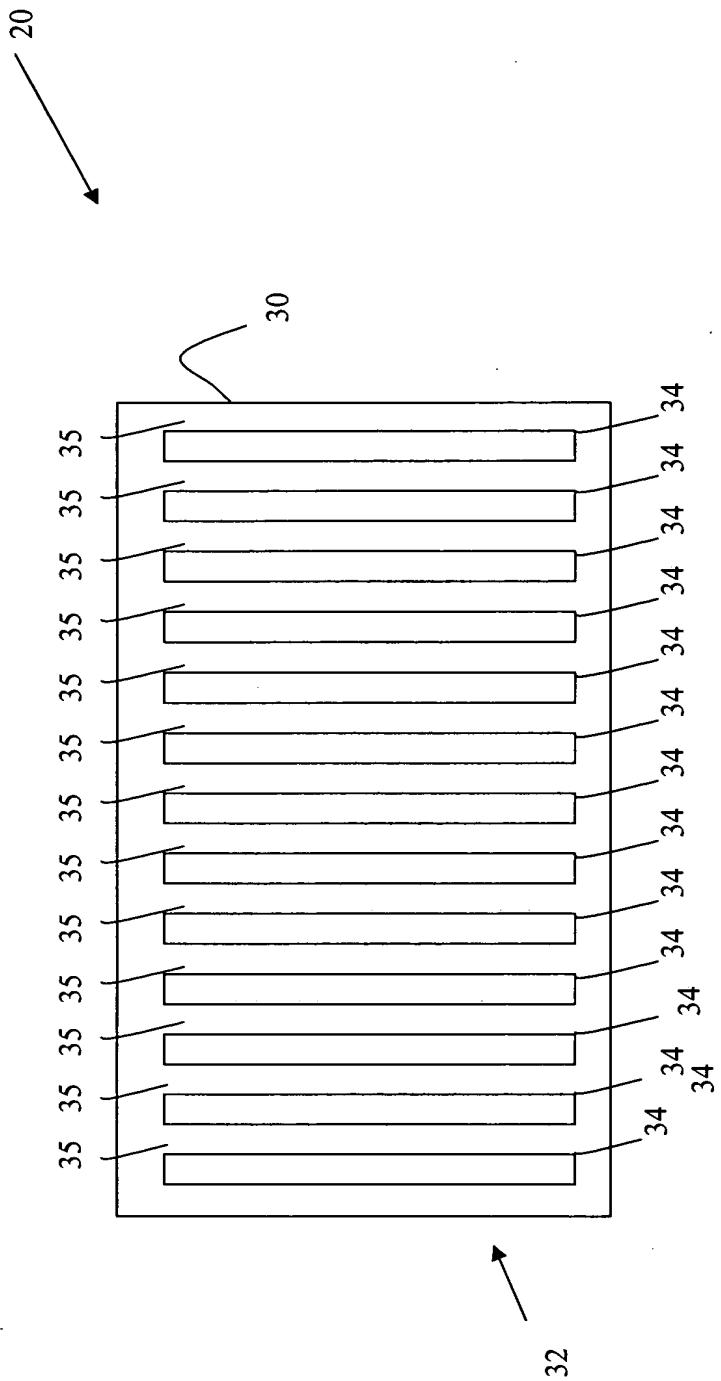


FIGURE 4

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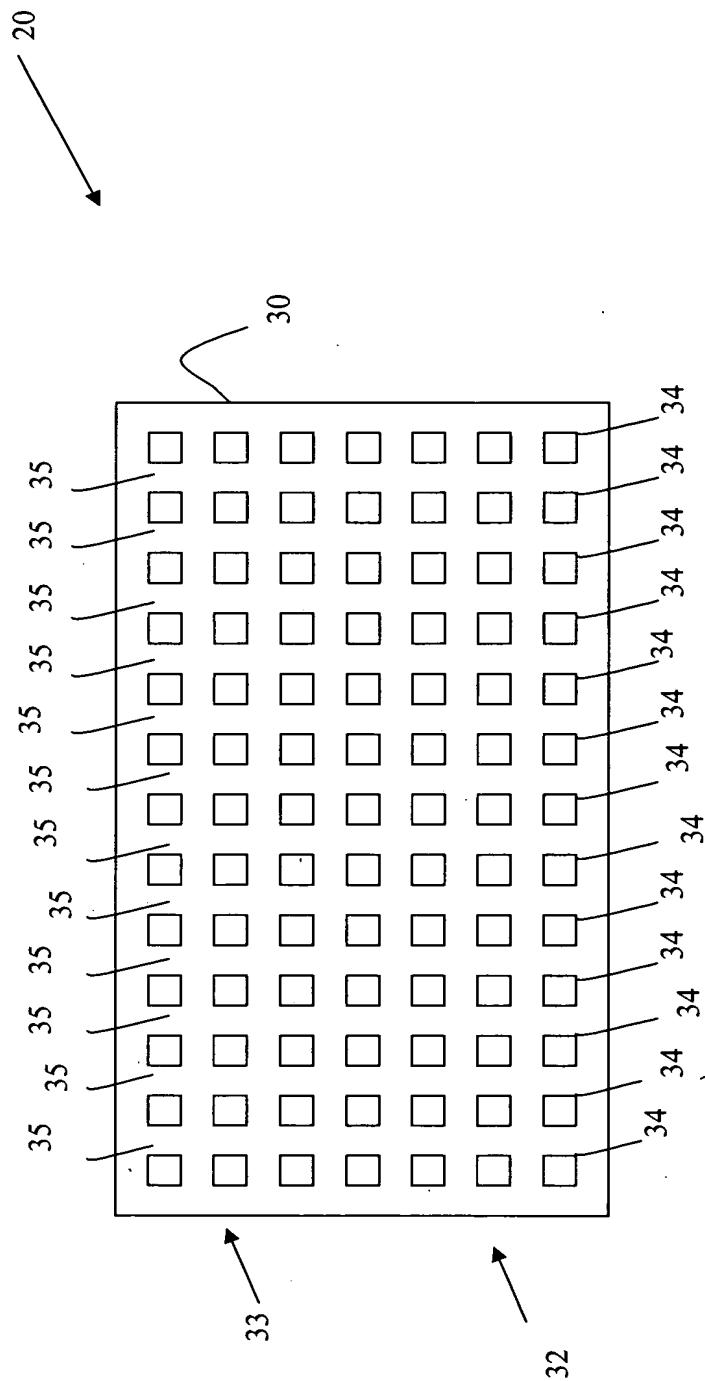


FIGURE 5

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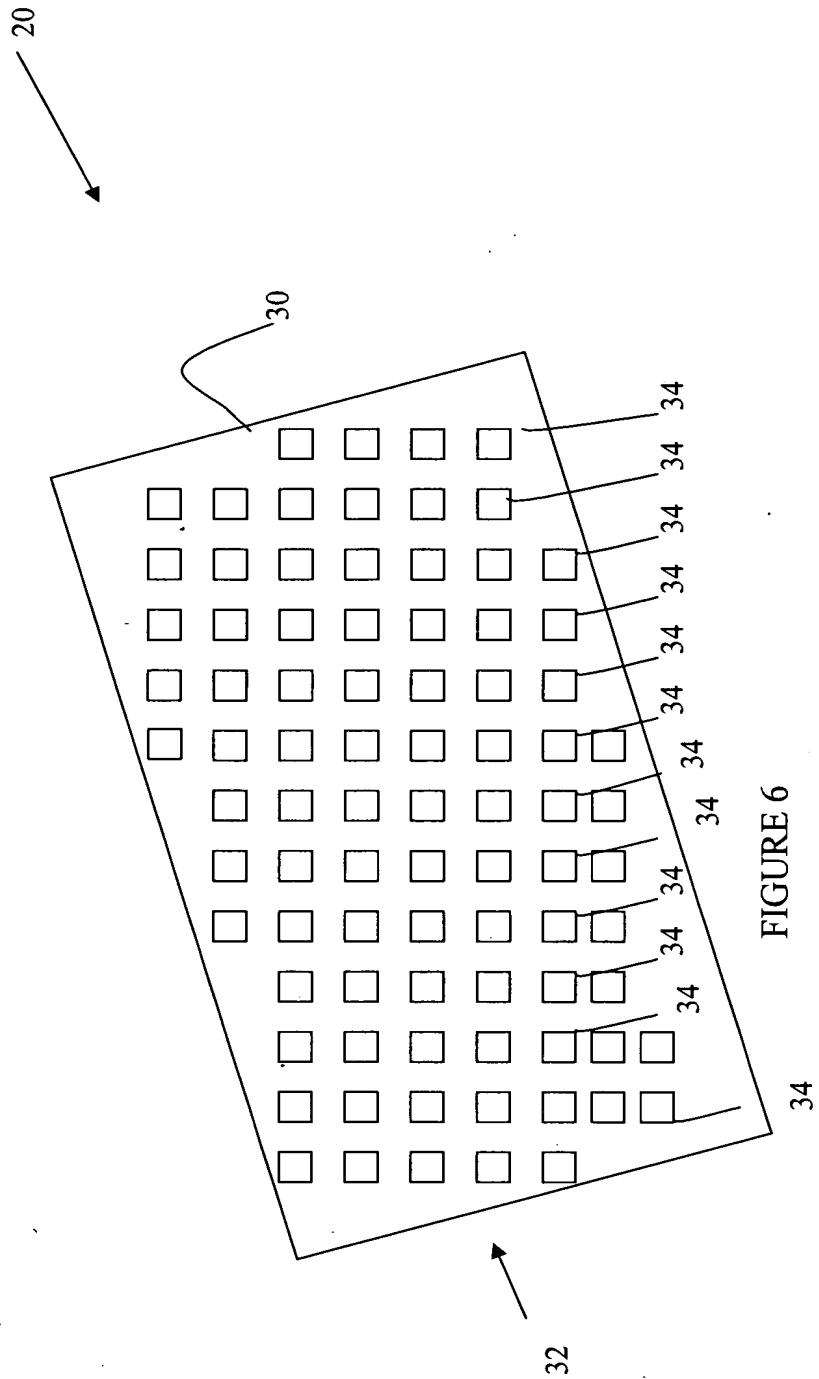


FIGURE 6

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